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				Application Number	10/707,546-Conf. #1545
				Filing Date	December 20, 2003
				First Named Inventor	Munehiro Karasudani
				Art Unit	2811
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	22040-00025-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear


FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>4</sup>

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NON PATENT LITERATURE DOCUMENTS			
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on	8A	"Multiple Twisted Dateline Techniques for Multigigabit DRAM's", Dong-Sun Min and Dietrich W. Langer, IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, pp. 858-865, June 1999.	

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Examiner Signature		Date Considered	04-14-06
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